

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Lance Lieberman (Reg. No. 28,437) on 7/9/2010.

Abstract:

A system for managing a resource in a terminal (10) for an architecture (15, 15') dedicated to a communications network. The system ~~comprises~~ **includes** a dedicated architecture resource manager (16, 16') adapted to process a request for a resource of said **the** dedicated architecture (15, 15') defined by a process manager (17, 17') of said **the** dedicated architecture (15, 15') as a function of an application activated on said **the** terminal (10) and to dialogue with a resource administrator (14) of a dedicated architecture manager (13) to manage a resource of said **the** terminal (10) and to process simultaneously the operation of said **the** dedicated architectures (15, 15') of said **the** terminal (10) that are connected to a plurality of said **the** communications networks. Application to the management of resources allocated to a communications network from a set of communications networks each offering a set of services via a dedicated architecture (15, 15') integrated in a terminal (10) connected to a public mobile network to which the user is a subscriber.

Claims:

1. **(Currently Amended)** A system for managing a resource in a multi-access point name (APN) terminal for a plurality of hardware architectures each dedicated to a corresponding one of a plurality of communications networks, wherein said system comprises;

a plurality of dedicated hardware architectures each comprising a plurality of dedicated architecture resource managers ~~each~~ configured to simultaneously process, on behalf of the ~~each~~ hardware architecture, requests defined by a process manager of the ~~each~~ hardware architecture for access to a common resource of the multi-APN terminal, the requests being generated as a function of an application activated on said multi-APN terminal, and

a dedicated hardware architecture manager comprising a resource administrator, wherein said each architecture resource manager is configured to simultaneously dialogue with a resource administrator of a dedicated hardware architecture manager of the multi-APN terminal to manage the common resource of said multi-APN terminal based on simultaneous operational processing of said plural dedicated hardware architectures of said multi-APN terminal which are each connected to the corresponding one of said plural communications networks.

Allowable Subject Matter

2. Claims 1-8 allowed.

The following is an examiner's statement of reasons for allowance:

Claims 1-8 are allowed over the prior art of record since the cited references taken individually or in combination fails to particularly disclose a dedicated hardware architecture manager comprising a resource administrator, wherein said each architecture resource manager is configured to simultaneously dialogue with a resource administrator of a dedicated hardware architecture manager of the multi-APN terminal to manage the common resource of said multi-APN terminal based on simultaneous operational processing of said plural dedicated hardware architectures of said multi-APN terminal which are each connected to the corresponding one of said plural communications networks.

It is noted that the closest prior art Gbadegesin et al. (US Patent No. 7,257,815 B2) teaches a system including a plurality of dedicated architecture resource managers each configured to simultaneously process, on behalf of each architecture a request defined by a process manager of each architecture. In addition, Kanai et al. (US Patent No. 6,714,946 B1) teaches resource manager for managing a plurality of modules for operating the computer system. However Gbadegesin in combination with Kanai fails to disclose or render obvious the above underlined limitations as claimed.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably

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accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

3. Any response to this action should be **faxed** to (571) 173-8300 or **mailed** to:

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Hand delivered responses should be brought to:

Customer Service Window
Randolph Building
401 Dulany Street
Alexandria, VA 22314

Any inquiry concerning this communication or earlier communications from the examiner should be directed to DANIEL MITCHELL whose telephone number is (571)270-5307. The examiner can normally be reached on Monday - Friday 8:00 am - 5:00 pm EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chirag G. Shah can be reached on 571-272-3144. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/D. M./
Examiner, Art Unit 2477

/Chirag G Shah/

Supervisory Patent Examiner, Art Unit 2477